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D. J. Mantem  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andrew J. Burstein et al. **FEB 19 2002** Art Unit : 2838  
Serial No. : 09/892,233 **TECHNOLOGY** Examiner : Unknown  
Filed : June 26, 2001  
Title : TRANSISTOR PATTERN FOR VOLTAGE REGULATOR

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Commissioner for Patents  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

Under 35 USC §120, this application relies on the earlier filing date of application serial number 09/498,297, filed on February 4, 2000. References AA-AD listed on the attached form PTO-1449 were submitted to and/or cited by the Patent Office in the prior application and, copies therefore, are not provided in this application.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 10/3/01

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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